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PATENT

Attorney Docket No. 211-178

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Michael J. UNDERHILL

Art Unit: Unassigned

Corresponding to
Application No. PCT/GB99/03776

Examiner: Unassigned

International Filing Date: November 12, 1999

For: ANTI-JITTER CIRCUITS

PRELIMINARY AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Prior to the examination of the above-identified patent application, please enter the following amendments and consider the following remarks.

IN THE CLAIMS:

Please amend claims 5, 7, 8, 12-14, 19, 21, 23, and 25 to the following form. An edited copy of the claims showing the amendments is attached hereto.

5. (Amended) An anti-jitter circuit as claimed in claim 2 wherein said means defining a negative feedback path comprises a low pass filter.

5. (Amended) An anti-jitter circuit as claimed in claim 2 wherein said mean d.c. voltage level is generated at an output of said negative feedback path and said means for comparing comprises a comparator having a first input coupled to the integrator charge storage means and a second input coupled to said output of the negative feedback path.